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U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM 10001421	FILING DATE 11/02/2001	CLASS 22851	SUBCLASS 738	GAU 1725	EXAMINER STONER-K
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****APPLICANTS:** Milewski Joseph; Woychik Charles;

****CONTINUING DATA VERIFIED:**
THIS APPLICATION IS A DIV OF 08/815,656 03/13/1997 PAT 6,330,967

**** FOREIGN APPLICATIONS VERIFIED:**

PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	ATTORNEY DOCKET NO
35 USC 112 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	END919970013US2
Verified and Acknowledged Examiners's initials	
TITLE : Low temperature solder chip attach structure	

U.S. DEPT. OF COM. / PAT. & TM. PTO-4361 / Rev. 12-91

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drawn	Figs. Drawn
		Print Fig.	
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	
		Application Examiner	
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